

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re Application of:

Yoji Takagi

Serial No.: 10/030,320

Filed: December 26, 2001

Confirmation No.: 5942

For: Wafer Supporting Device In
Semiconductor
Manufacturing Device§
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§

Group Art Unit: 1763

Examiner: Unknown

Office of Initial Patent Examination
Box: Filing Receipt Corrections
Commissioner for Patents
Washington, D.C. 20231

Dear Sir:

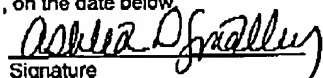
CERTIFICATE OF FACSIMILE
TRANSMISSION UNDER 37 CFR 1.8

I hereby certify that this correspondence and the documents referred to as attached therein are being facsimile transmitted to the U.S. Patent and Trademark Office to the fax number 703-746-9195 to: Office of Initial Patent Examination, Box: Filing Receipt Corrections, Commissioner for Patents, Washington, D.C. 20231, on the date below.

02-21-2003

Date

Signature

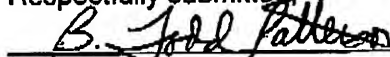


REQUEST FOR CORRECTION OF ERRORS ON FILING RECEIPT

The Filing Receipt for the above-referenced application contains an error regarding the filing date. The Filing Receipt recites the filing date as [12/22/2001]. Please delete [12/22/2001] and insert the correct date 12/26/2001. The correct filing date should read as 12/26/2001.

Attached is a copy of the Filing Receipt with the above changes noted thereon. Also attached is a copy of the Return postcard and the Express Mail Receipt, which were both acknowledged on December 26, 2001. Please correct this error or notify the Applicants of the reasons for denying the request.

Respectfully submitted,



B. Todd Patterson
Registration No. 37,906
MOSER, PATTERSON & SHERIDAN, L.L.P.
3040 Post Oak Blvd., Suite 1500
Houston, TX 77056
Telephone: (713) 623-4844
Facsimile: (713) 623-4846
Attorney for Applicants



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UNITED STATES PATENT AND TRADEMARK OFFICE
WASHINGTON, D.C. 20231
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APPLICATION NUMBER	FILING DATE	GRP ART UNIT	FIL FEE REC'D	ATTY. DOCKET NO.	DRAWINGS	TOT CLAIMS	IND CLAIMS
10/030,320 ✓	12/22/2001 12/26/2001 ✓	1763 ✓	890	SOEI/0020 ✓	6 ✓	8 ✓	1 ✓

CONFIRMATION NO. 5942 ✓

Moser Patterson & Sheridan
3040 Post Oak Blvd Suite 1500
Houston, TX 77056 ✓

*correct
filing date*

FILING RECEIPT



OC000000007663008

Date Mailed: 03/21/2002

Receipt is acknowledged of this nonprovisional Patent Application. It will be considered in its order and you will be notified as to the results of the examination. Be sure to provide the U.S. APPLICATION NUMBER, FILING DATE, NAME OF APPLICANT, and TITLE OF INVENTION when inquiring about this application. Fees transmitted by check or draft are subject to collection. Please verify the accuracy of the data presented on this receipt. If an error is noted on this Filing Receipt, please write to the Office of Initial Patent Examination's Customer Service Center. Please provide a copy of this Filing Receipt with the changes noted thereon. If you received a "Notice to File Missing Parts" for this application, please submit any corrections to this Filing Receipt with your reply to the Notice. When the USPTO processes the reply to the Notice, the USPTO will generate another Filing Receipt incorporating the requested corrections (if appropriate).

Applicant(s)

✓ Yoji Takagi, Narita-shi, Chiba, JAPAN; ✓

Domestic Priority data as claimed by applicant ✓ ✓

THIS APPLICATION IS A 371 OF PCT/JP01/03632 04/26/2001

Foreign Applications ✓

JAPAN P2000-13195 04/28/2000 ✓

Projected Publication Date: To Be Determined - pending completion of Security Review

Non-Publication Request: No

Early Publication Request: No

Title ✓

Wafer supporting device in semiconductor manufacturing device

Preliminary Class

118

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